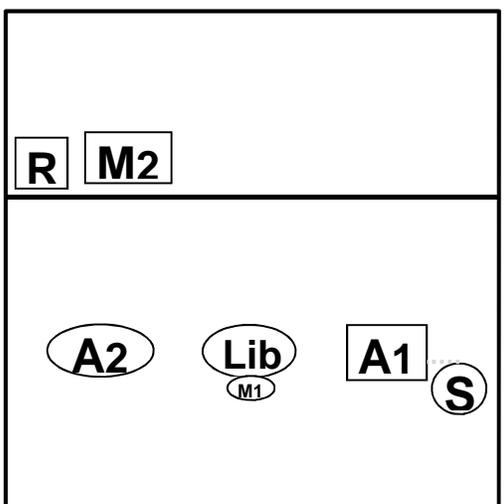


1

R	M2	A1
A2	M1	S

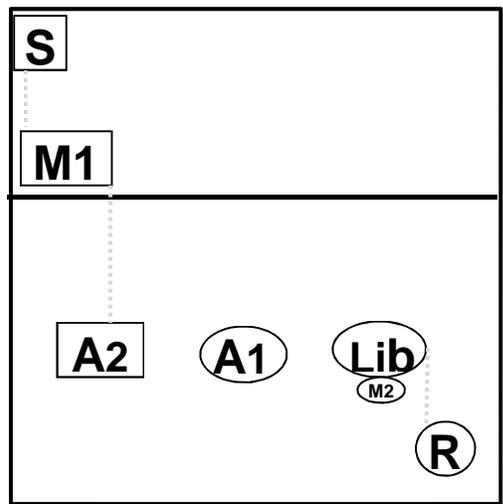
(A-M-R A-M-S)



4

S	M1	A2
A1	M2	R

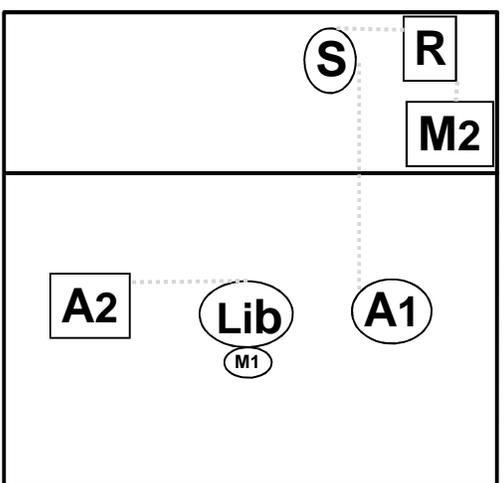
(A-M-S A-M-R)



2

A2	R	M2
M1	S	A1

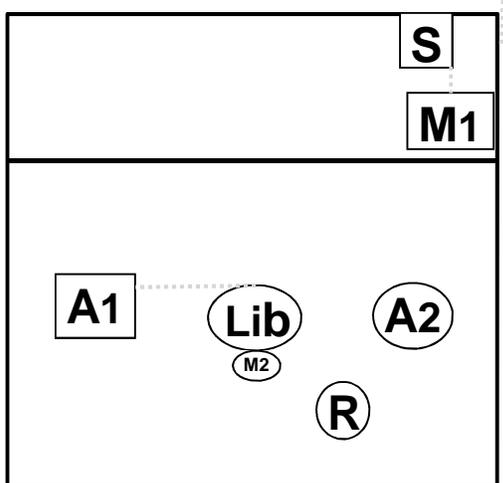
(MmRah MmSah)



5

A1	S	M1
M2	R	A2

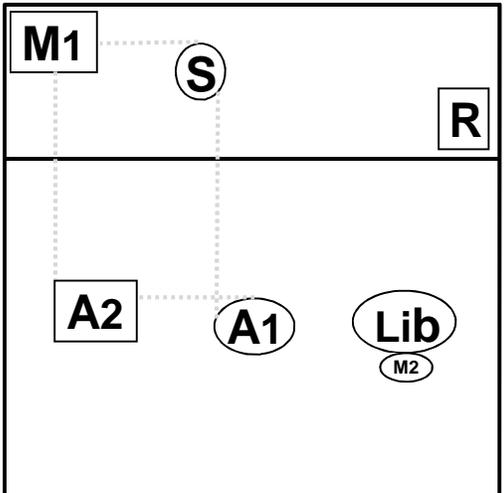
(MmSah MmRah)



3

M1	A2	R
S	A1	M2

(RAM SAM)



6

M2	A1	S
R	A2	M1

(SAM RAM)

